

## Matthew A. Krass

### Present Address

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### Permanent Address

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### Objective

Computer or Software Engineering Summer Internship

### Education

Undergraduate Computer Engineering, Spr 2010, Clarkson University, Potsdam, NY

### Experience

**Surveillance Installation**, Holbrook, NY Summer 2006 to Present

- Planned wiring layout for camera installation
- Constructed, installed and configured Digital Video Recorder units
- Configured and supported remote secured viewing systems

**EE211 ECE Sophomore Lab**, Clarkson University, Potsdam, NY Spring 2008

- Basic construction and analysis of electrical circuits
- Creation of Virtual Instruments
- Proper engineering documentation of a system

**EE365 Adv Digital Design**, Clarkson University, Potsdam, NY Fall 2008

- Advanced use of VHDL for systems design
- Use of Karnaugh maps to determine simplified logic expressions
- Extensive testing and debugging techniques, including documentation

**EE316 Comp Eng Junior Lab**, Clarkson University, Potsdam, NY Spring 2009

- Open-ended circuit design to meet specifications
- Extensive designing, documentation, testing and presentation
- Group work distribution and collaboration

**EE360 Microprocessors**, Clarkson University, Potsdam, NY Spring 2009

- Basic embedded programming on Motorola HCS12 microcontrollers
- Basic microcontroller components and interfacing
- Basic microcontroller simulation and debugging

### Extracurricular Activities (cont'd on back)

**FIRST Robotics** 2002 to 2008

- Embedded programming and autonomous operation
- Sensor based feedback and closed loop control
- Electrical wiring for controls and actuators

**Clarkson University Amateur Radio (K2CC)** 2006 to Present

- FCC Licensed as Amateur General

**Clarkson SPEED Mini Baja** 2006 to 2007

- Designed onboard data acquisition computer
- Sensor based electronic dashboard

**Clarkson SPEED Radio**

2007 to Present

- Application of amateur radio technology to emergency communications
- Application of D\*STAR technology to amateur radio

**Skills**

<u>Languages:</u>	Java, C++, C, VHDL, PHP, Perl, MySQL, LaTeX
<u>Software:</u>	EAGLE Layout Editor, Quartus II, PSPice, MATLAB, LabView, Microsoft Office 2007, Agilent Logic Analyzer
<u>Test Equipment:</u>	Oscilloscope, Multimeter, LCR Meter, Function Generator, Logic Analyzer
<u>Circuitry:</u>	Soldering, Desoldering, PCB layout, Breadboarding